

PATENT NUMBER

U.S. UTILITY Patent Application

	O.I.P.E.
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PATENT DATE

SCANNED

Q.A

APPLICATION NO.
09/738193

CONT/PRIOR

CLASS
438

SUBCLASS

ART UNIT

EXAMINER

APPLICANTS

Technique Details

System, method and apparatus for constructing a semiconductor wafer inductor using 3D layer techniques

PTO-2040
12/89**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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